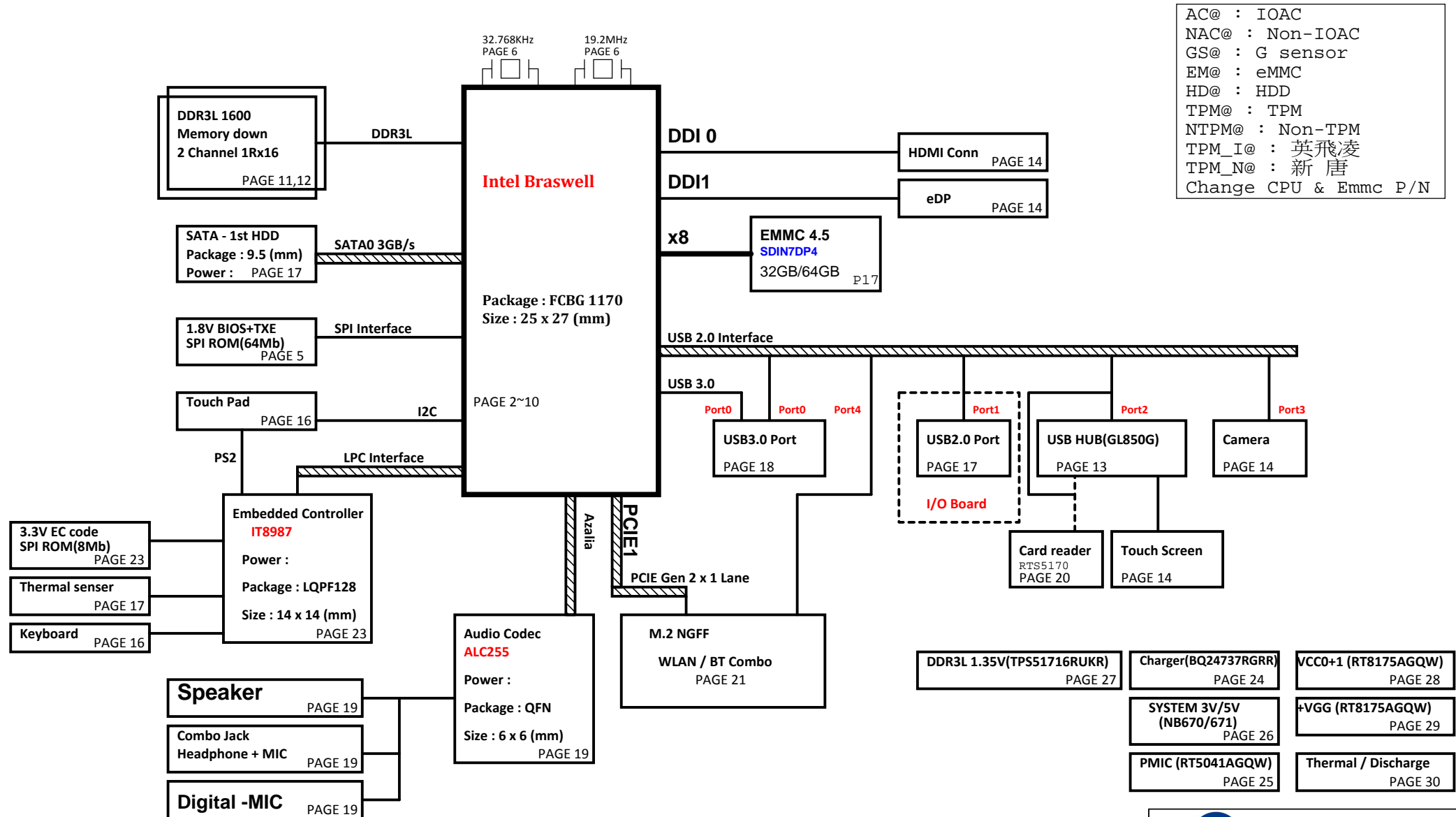


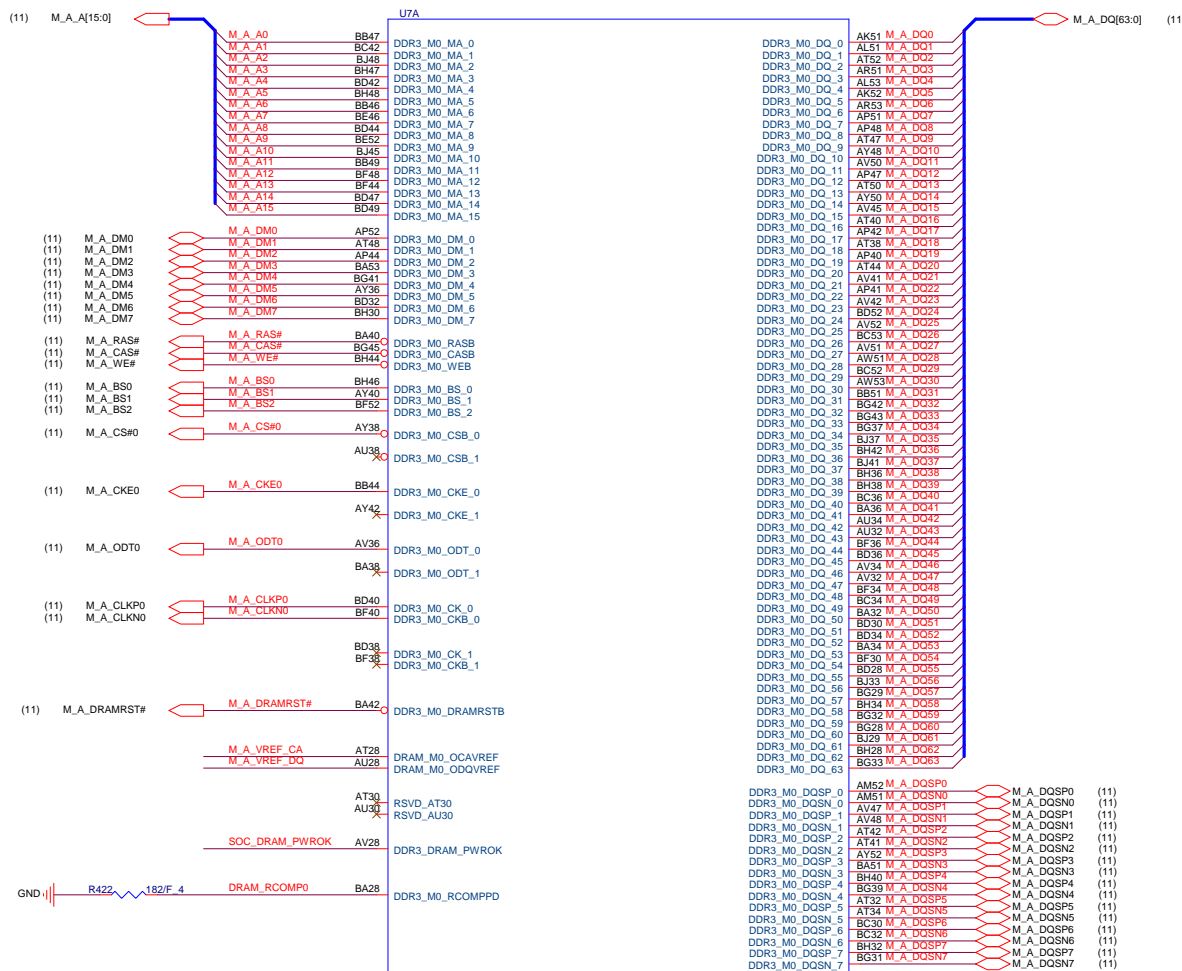
ZHX_ZHXS UMA(11.6")

Intel Braswell Platform Block Diagram

1

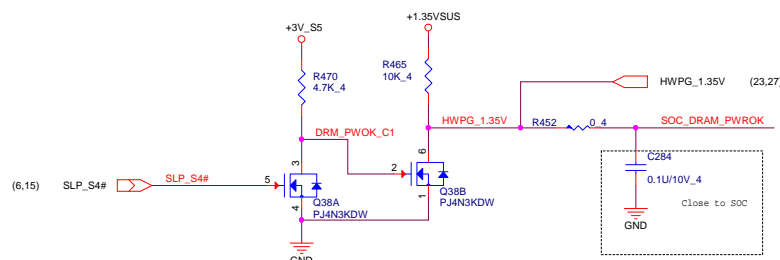


+1.35V/SUS (3,9,11,12,22,27)
+3V_S5 (3,5,9,13,15,16,18,23,25)



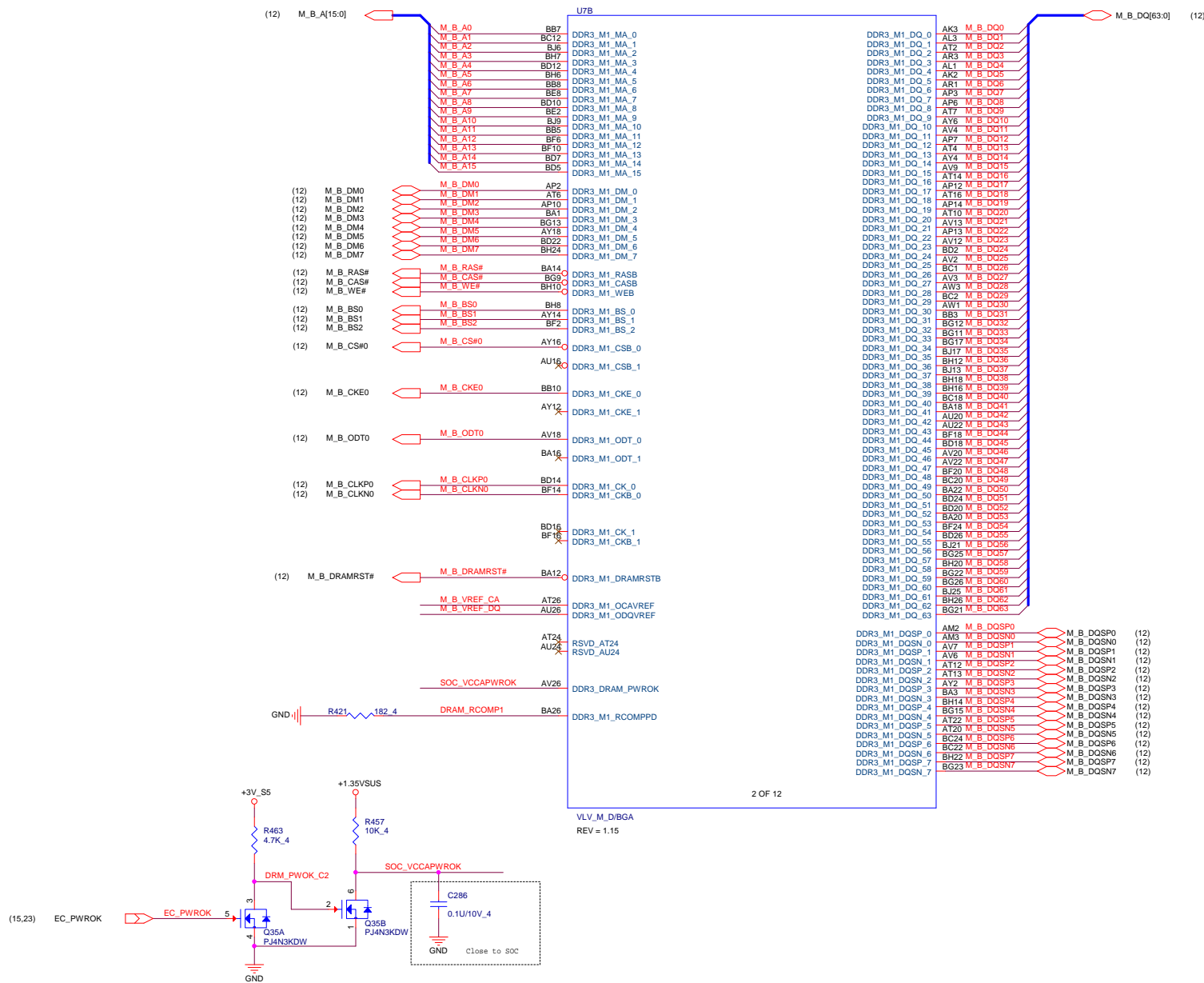
Channel 0	Channel 1	SOC Supported Memory Operation Speed
1333 MHz	X	1066 MHz
1600 MHz	X	1600 MHz
1333 MHz	1333 MHz	1066 MHz
1600 MHz	1600 MHz	1600 MHz

Channel 0 need to
be populated first for the platform to power on



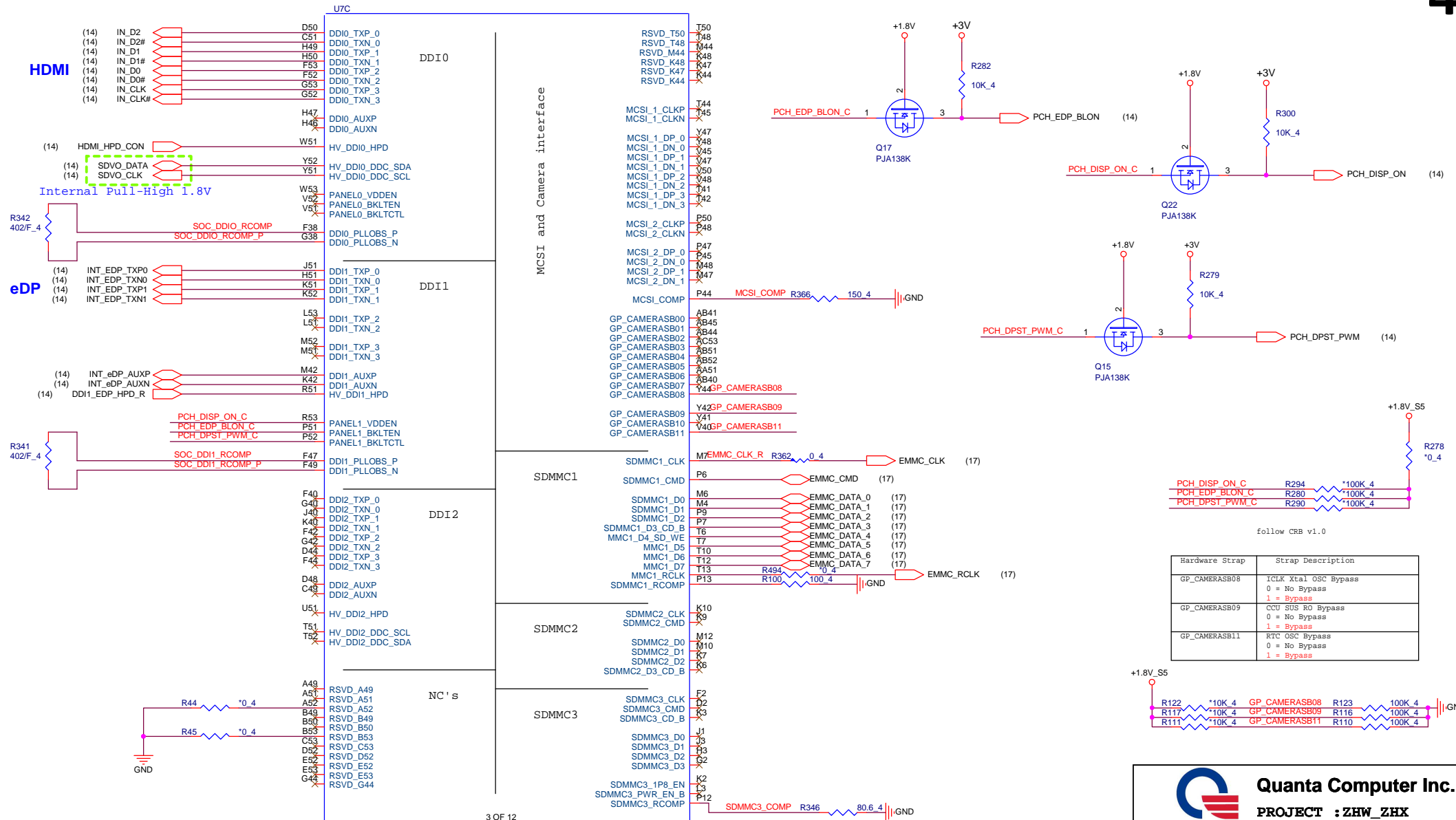
Quanta Computer Inc.
PROJECT : ZHW_ZHX

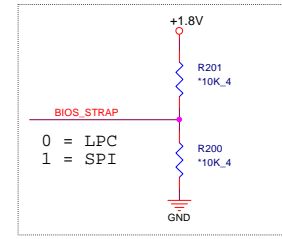
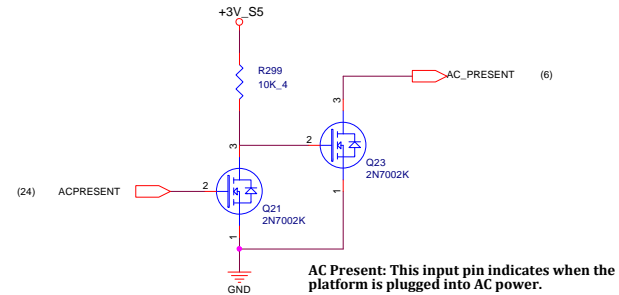
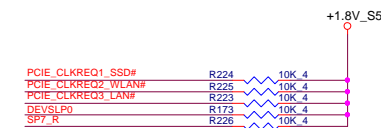
Size	Document Number	Rev
	Valley 1/9 (DDRA)	1A
Date:	Friday, February 05, 2016	Sheet 2 of 31

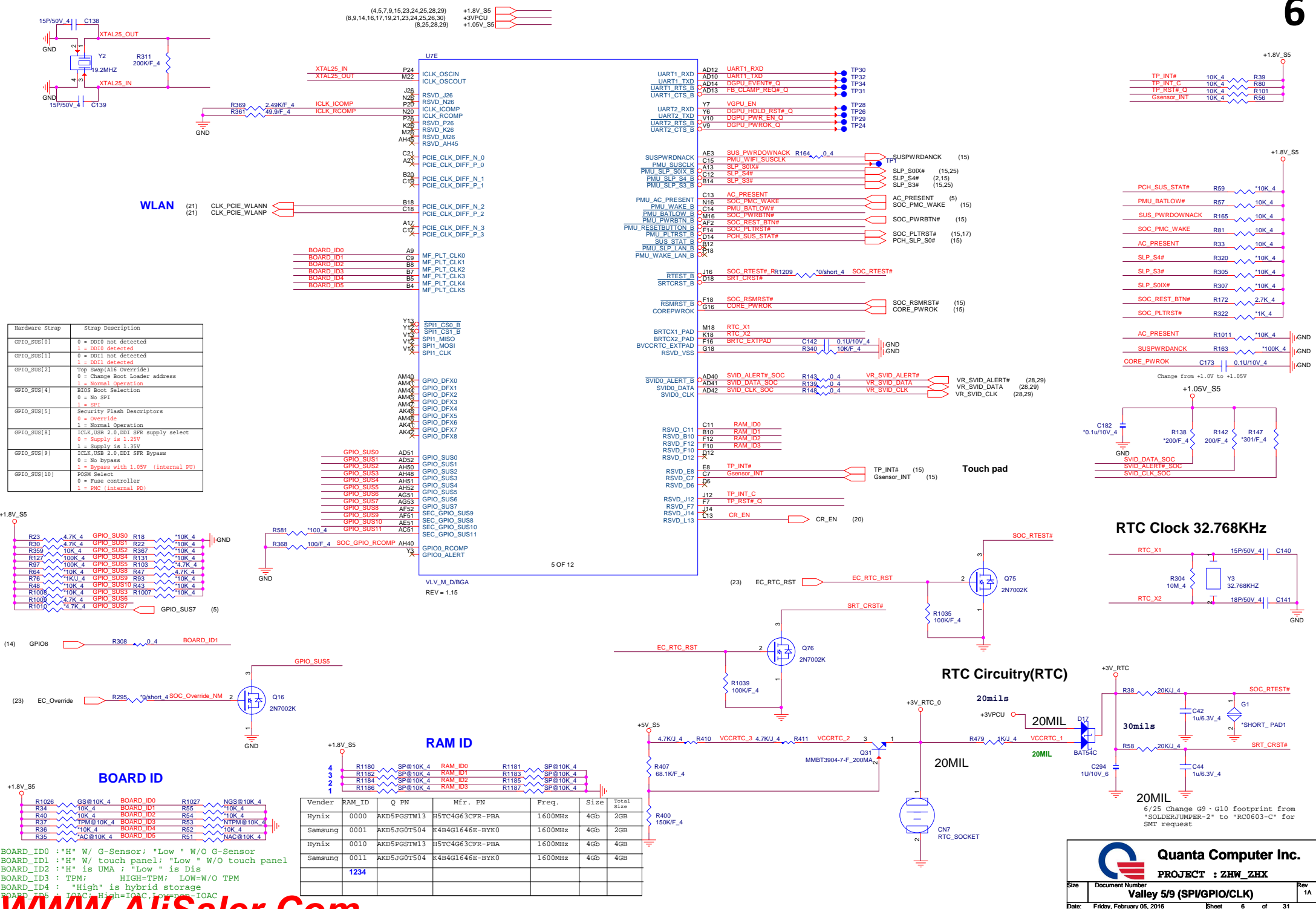


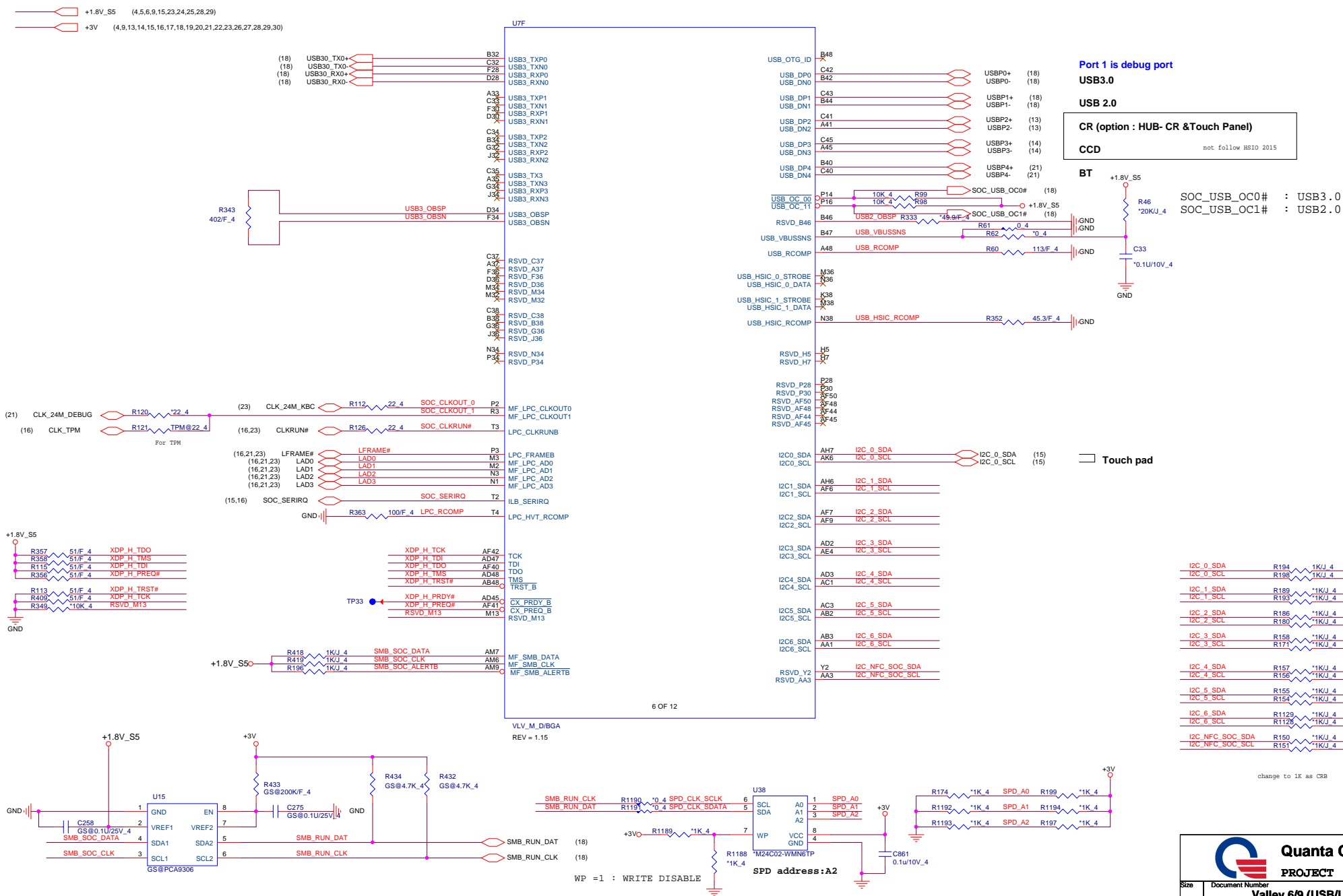
(7,9,13,14,15,16,17,18,19,20,21,22,23,26,27,28,29,30)

+3V

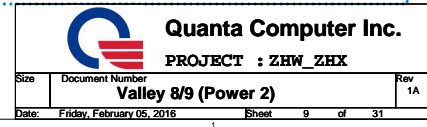


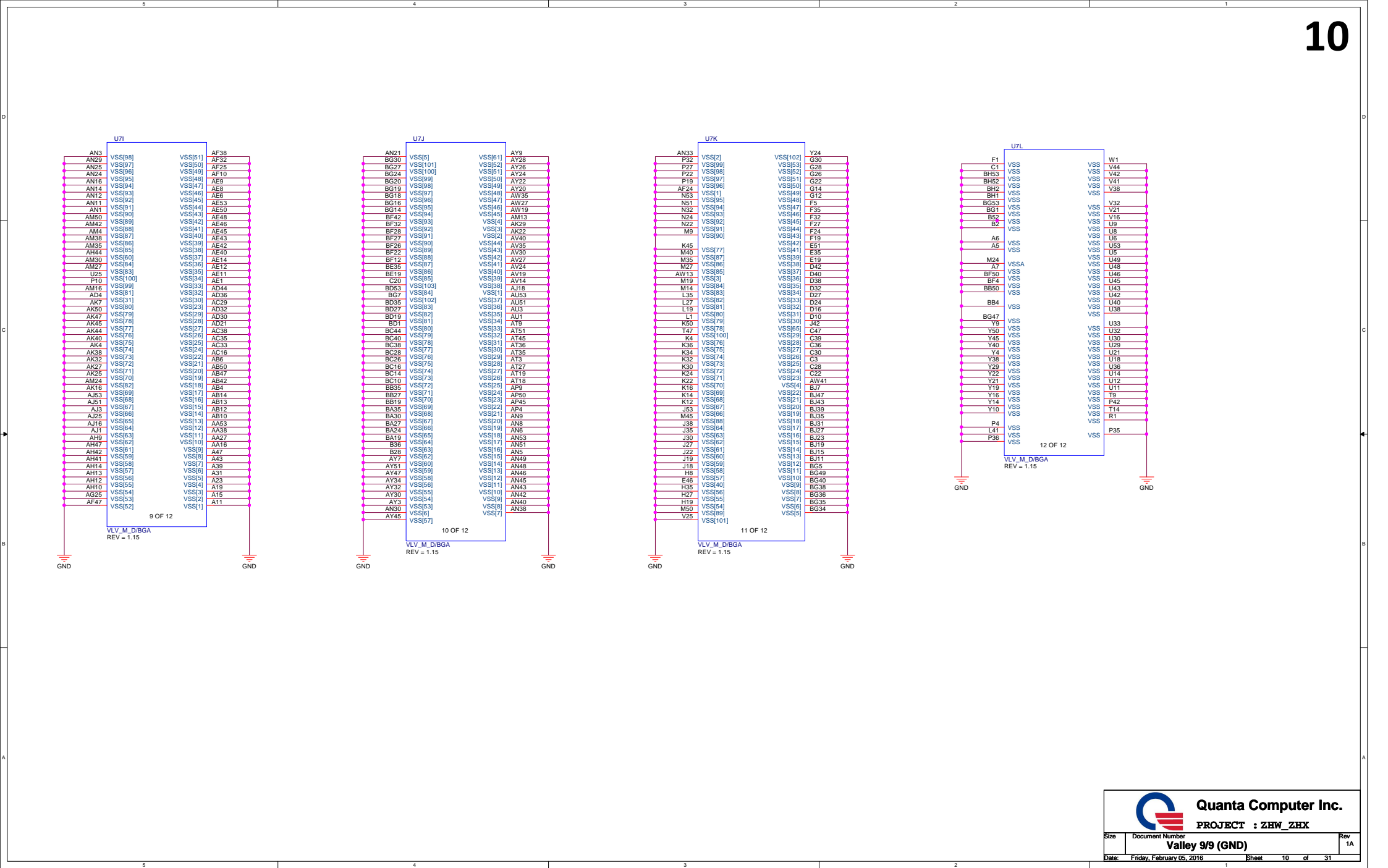









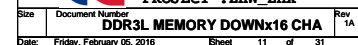






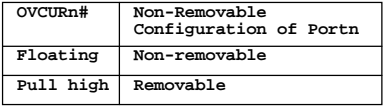
Quanta Computer Inc.
PROJECT : ZHW_ZHX

Size	Document Number	Rev
	Valley 9/9 (GND)	1A
Date:	Friday, February 05, 2016	Sheet 10 of 31

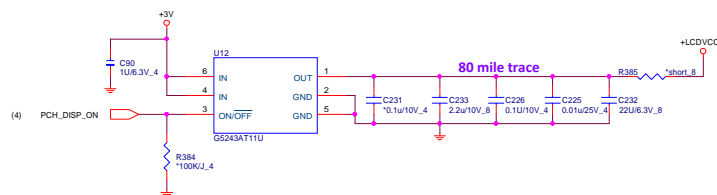




13

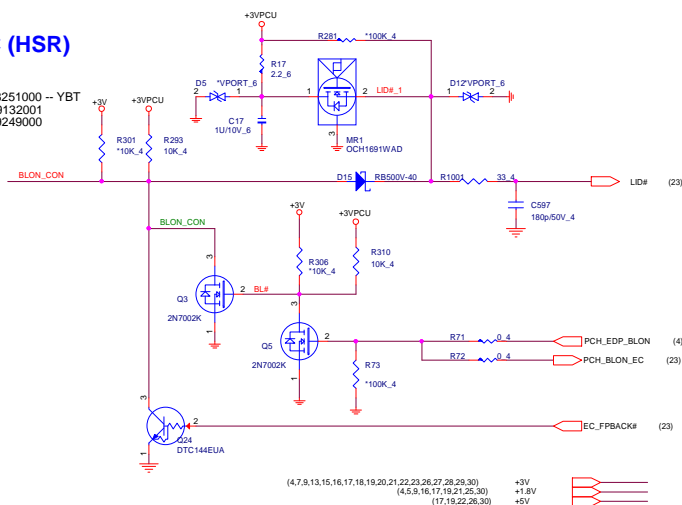


LVDS Conn.



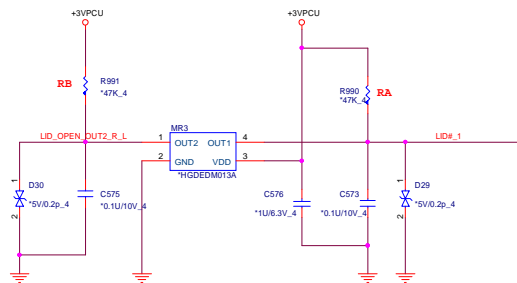
HALL IC (HSR)

1st source : EOD
2nd source : AL008251000
3rd source : AL009132001
4th source : AL009249000

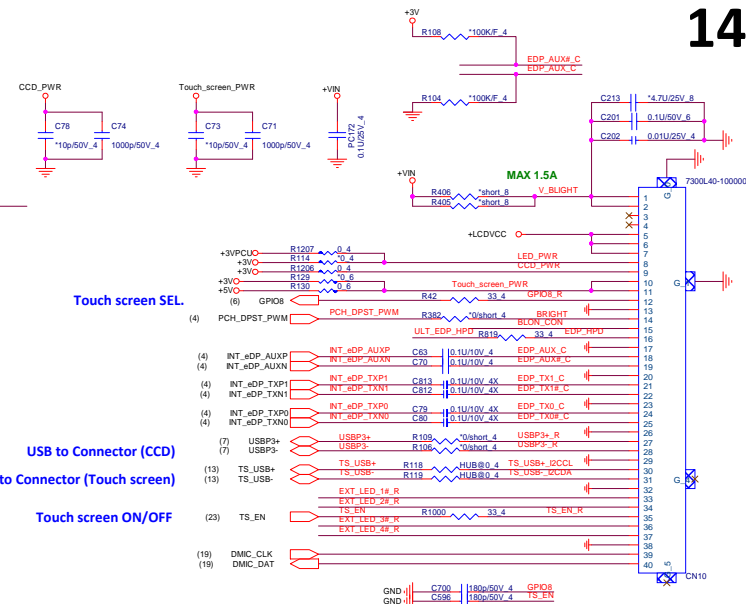


GMR Sensor

RA/RB can be unstuffed if using GMR



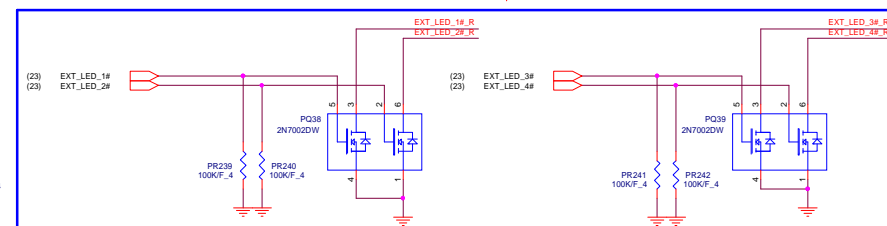
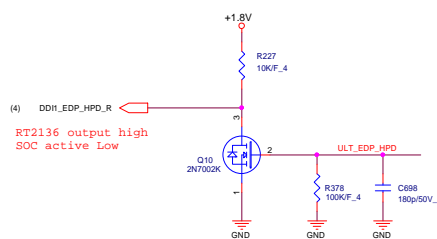
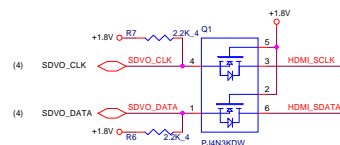
Touch screen SEL



USB to Connector (CCD)

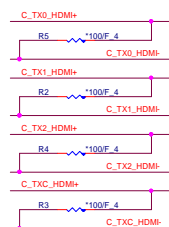
USB to Connector (Touch screen)

Touch screen ON/OFF

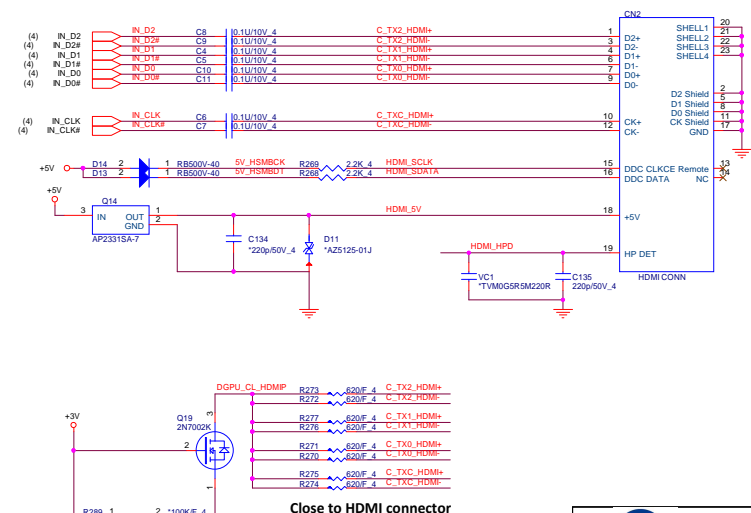
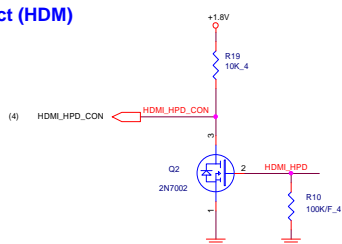
**HDMI Conn.**

HDMI SMBus Isolation

EMI (EMC)

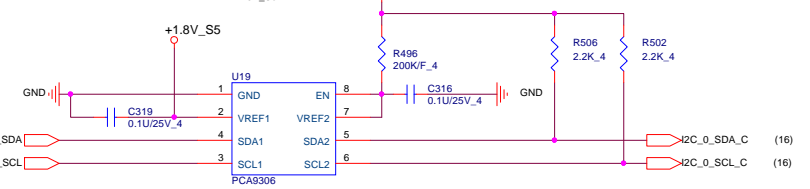
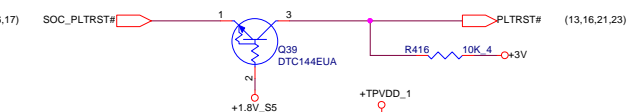
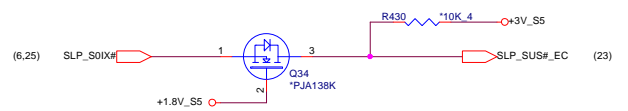
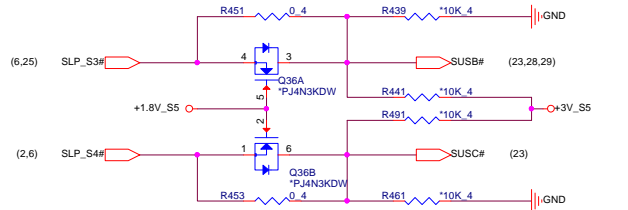
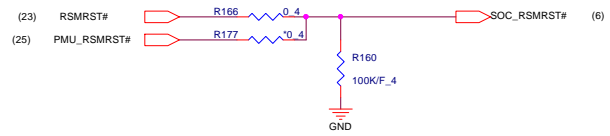
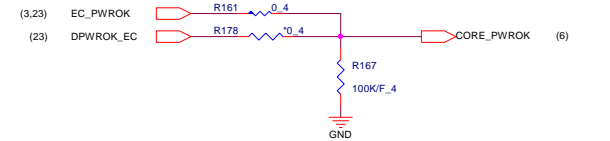
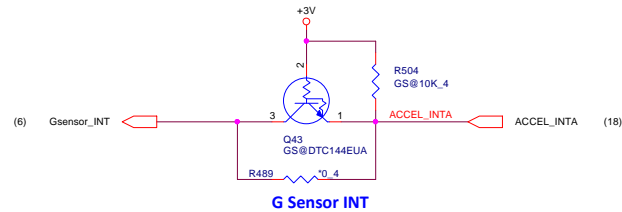
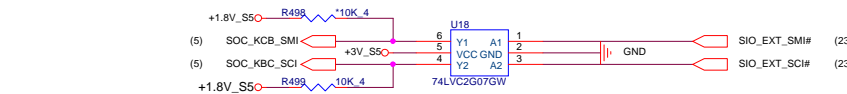
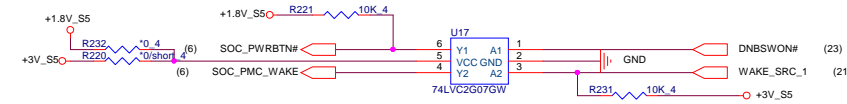
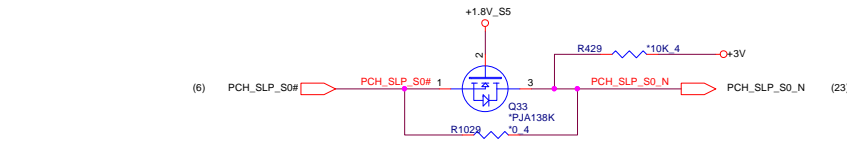
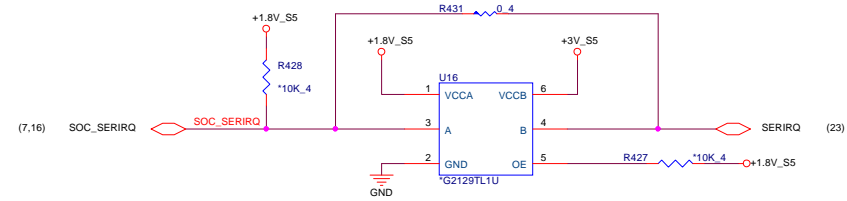


HDMI-detect (HDM)

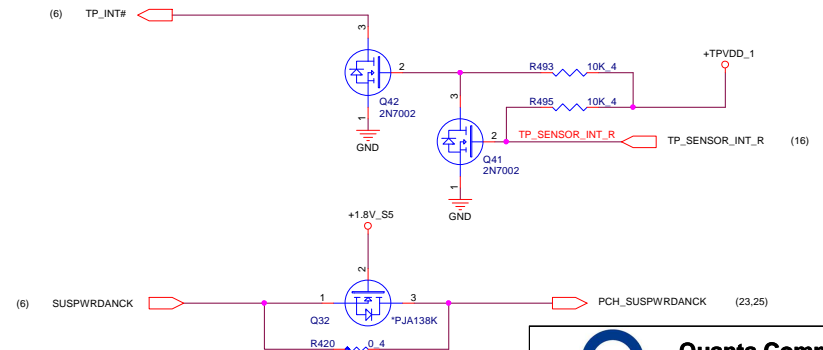


Close to HDMI connector

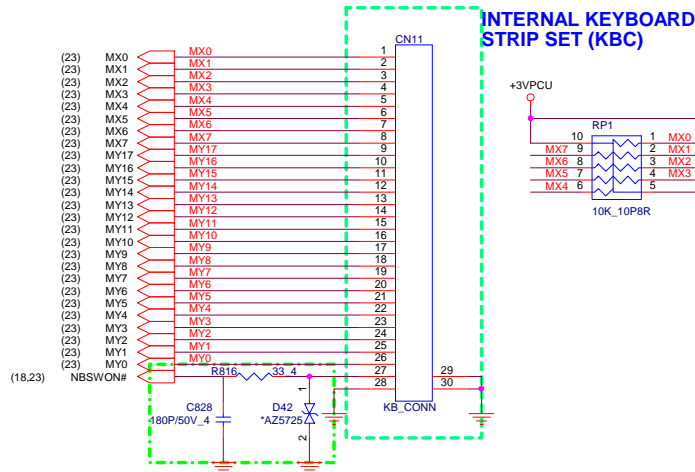
+1.8V_S5 (4,5,6,7,9,23,24,25,28,29)
 +3V_S5 (2,3,5,9,13,16,18,23,25)
 +1.8V (4,5,9,14,16,17,19,21,25,30)
 +3V (4,7,9,13,14,16,17,18,19,20,21,22,23,26,27,28,29,30)



For Touch pad : POWER-A

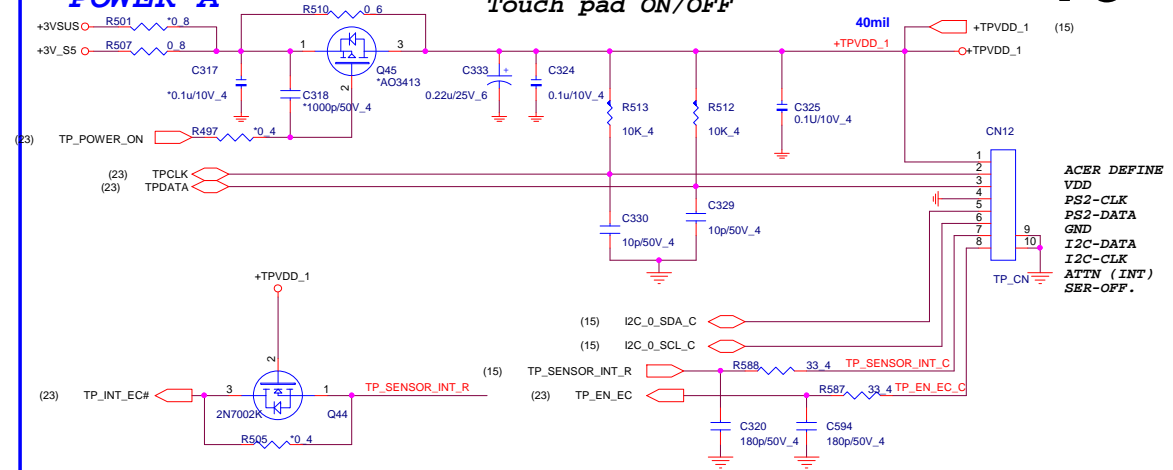


KEYBOARD (KBC)



TOUCH PAD (TPD)

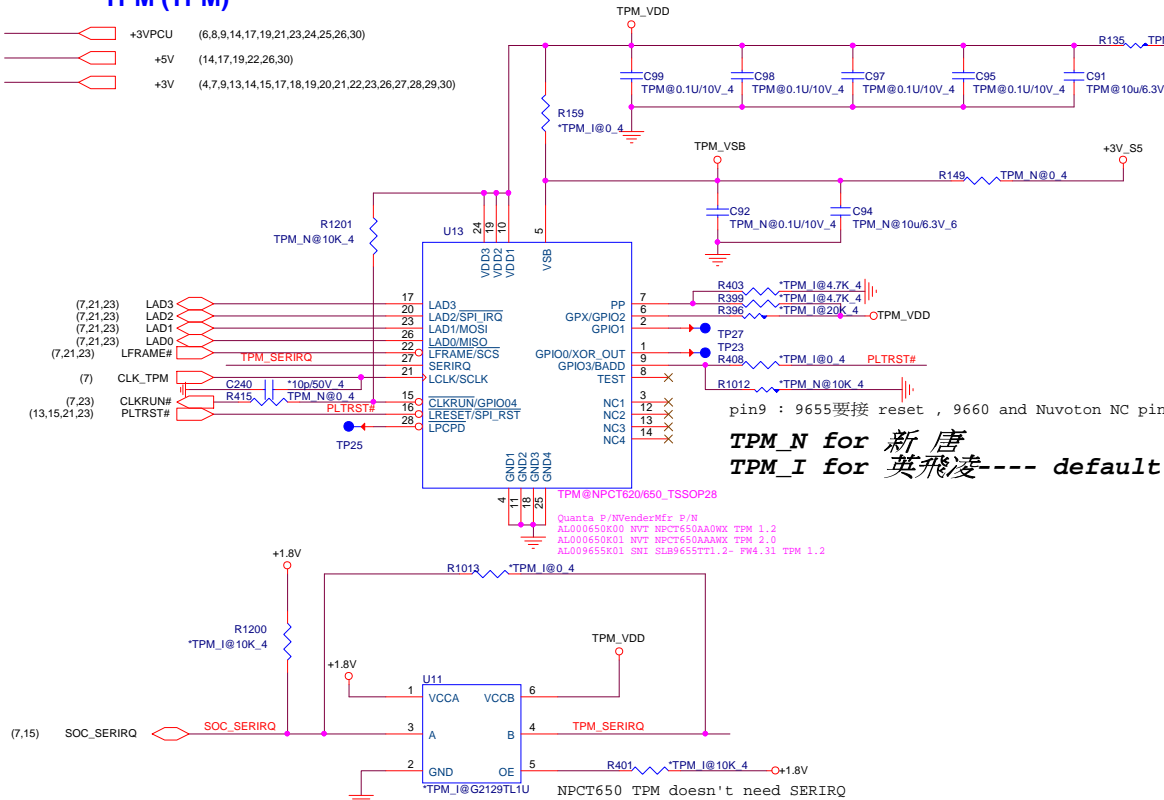
POWER-A



16

ACER DEFINE
VDD
PS2-CLK
PS2-DATA
GND
I2C-DATA
I2C-CLK
ATTN (INT)
SER-OFF.

TPM (TPM)



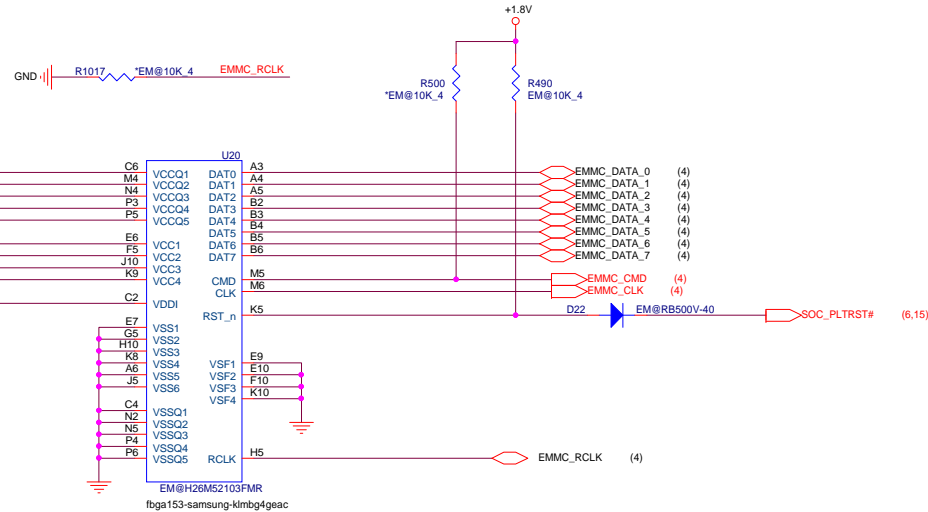
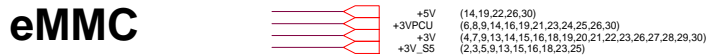
note: serie need to add level shift



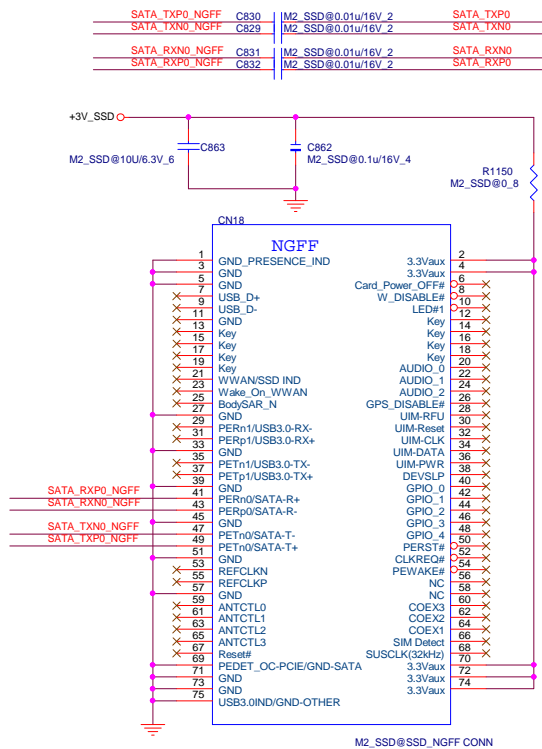
Quanta Computer Inc.

PROJECT : ZHW_ZHX

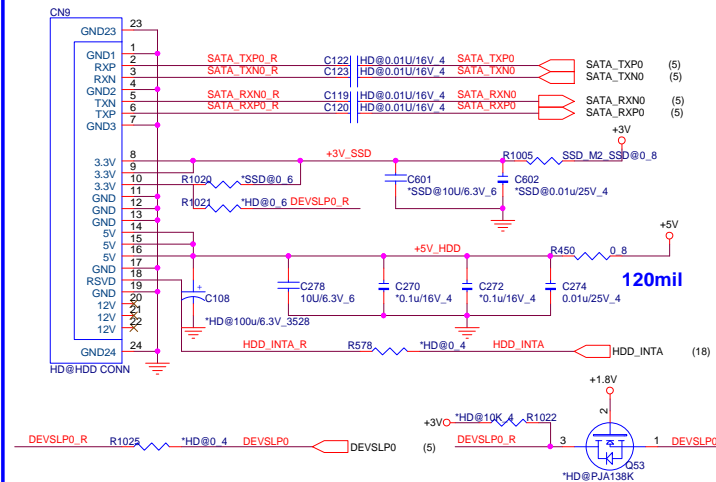
Size	Document Number	Rev
	KB/BT/TP	1A
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Vendor	P/N
HYNIX 32G	AKE3SZ-TW09
Kingston 32G(ZHX)	AR0ZDAR1001
Kingston 32G(ZHXS)	AKE3SZ-TP06

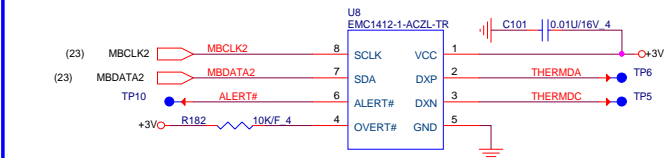


SATA HDD



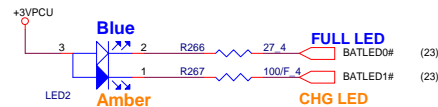
P/N:DFHS22FR463
2nd source : DFHS22FR501

CPU Thermal sensor(THS) / MB Local TEMP

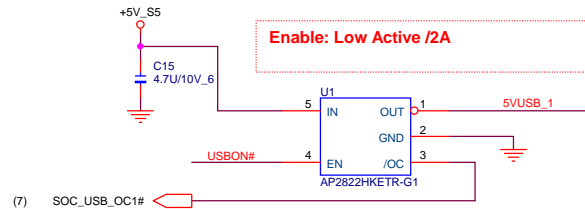
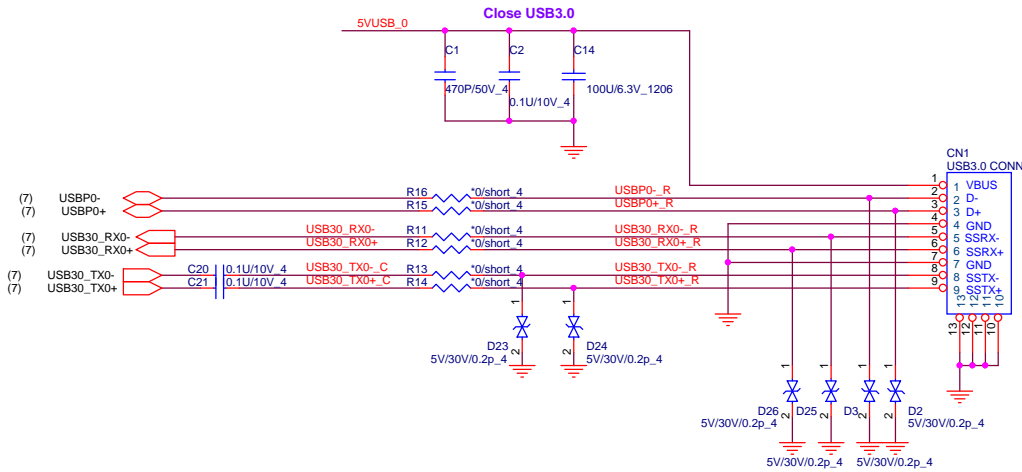
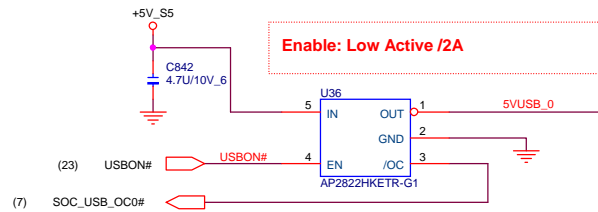


Main:AL001412003 EMC1412-1-ACZL-TR(98h)
2nd:AL000431014 TMP431ADGKR(98h)

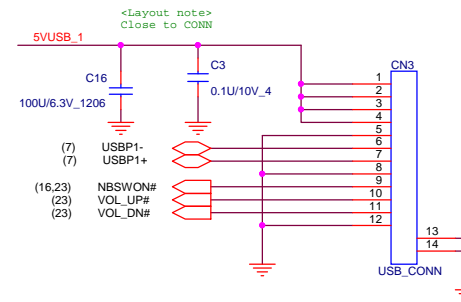
Battery indicator



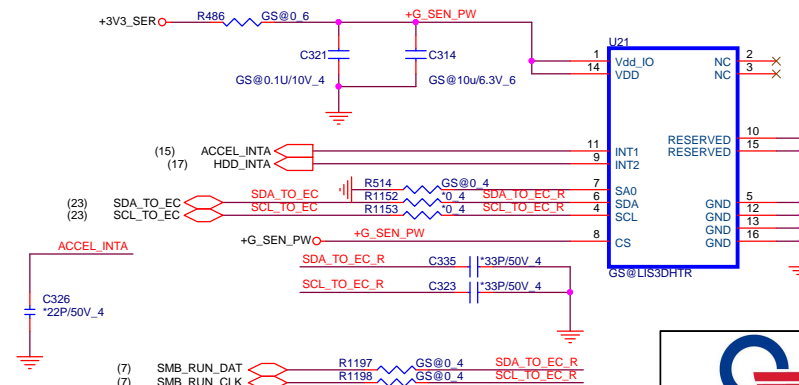
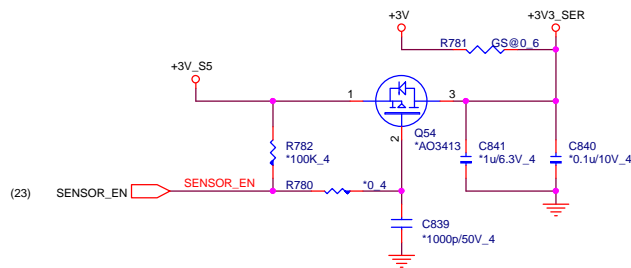
USB 3.0 Connector



D/B Port USB 2.0



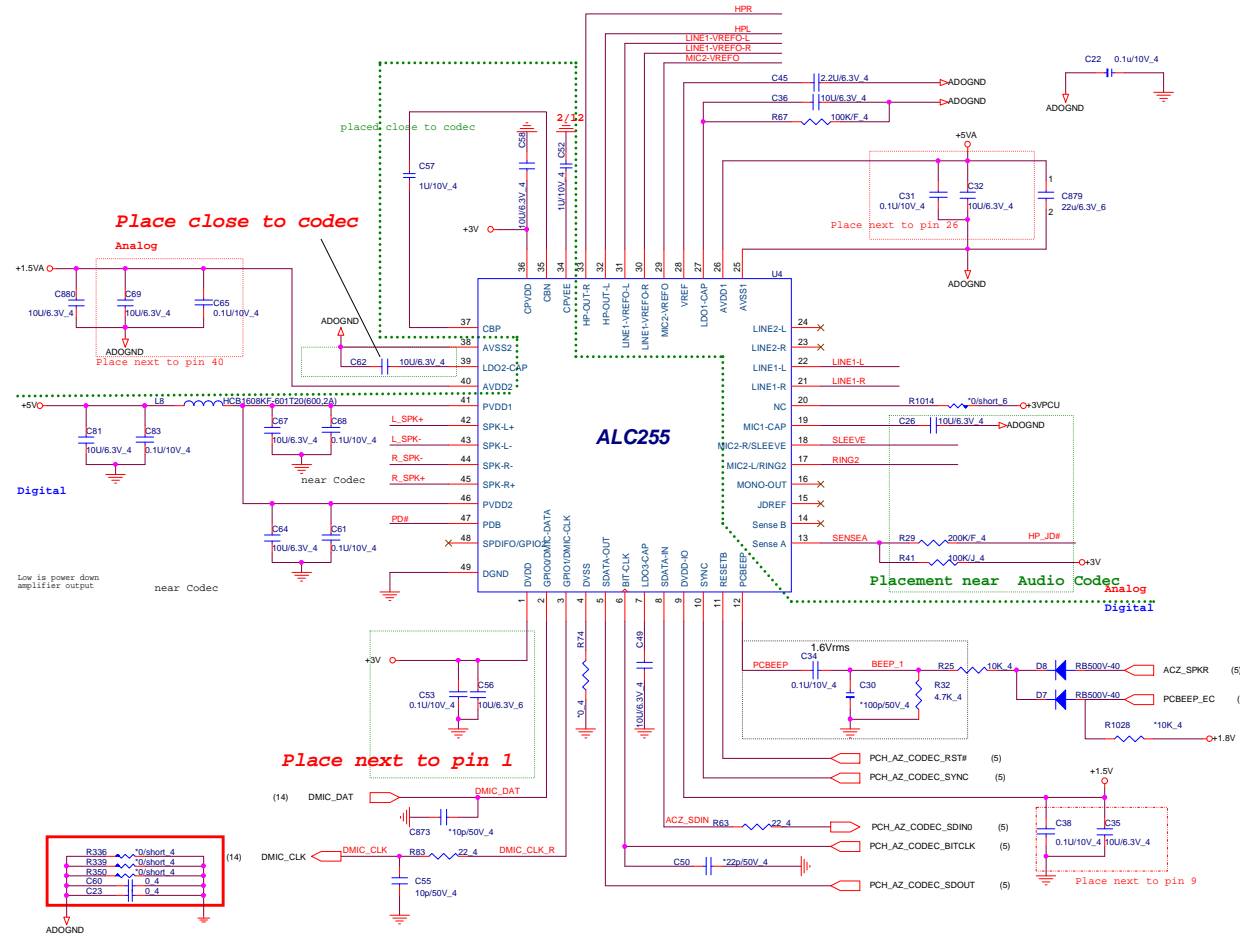
G-sensor



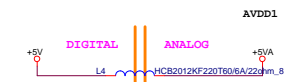
Codec(ADO)

(14,17,22,26,30)

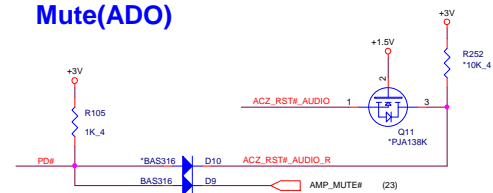
+5V



Codec PWR 5V(ADO)



Mute(ADO)



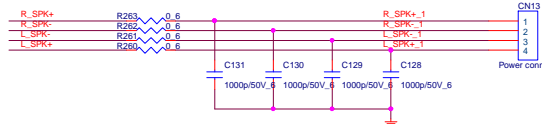
Codec PWR 1.5V(ADO)



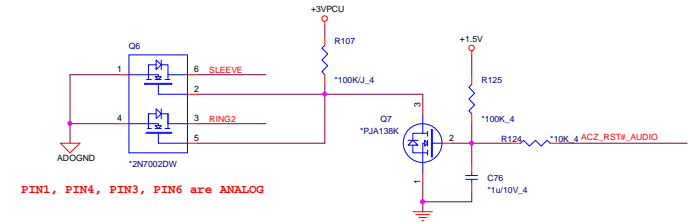
Internal Speaker

footprint 88266-040xx-xxx-4p-1

40mil for each signal



Grounding circuit(ADO)



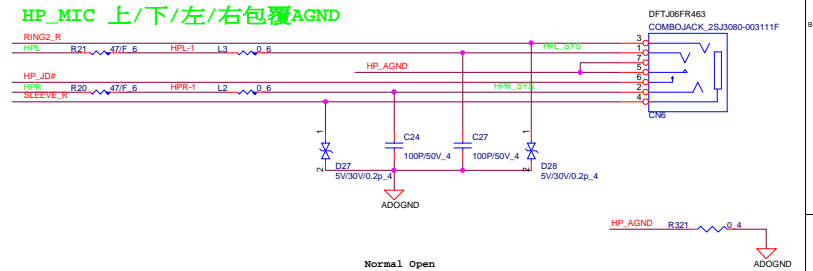
Microphone

move to LED BD

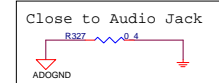
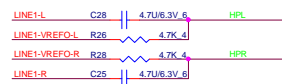
note : change next P/N: DFTJ06FR653
CONN DIP PHONE JACK 6P FR(H4.5)
foot print: phjk-2sj3072-108111f-6p

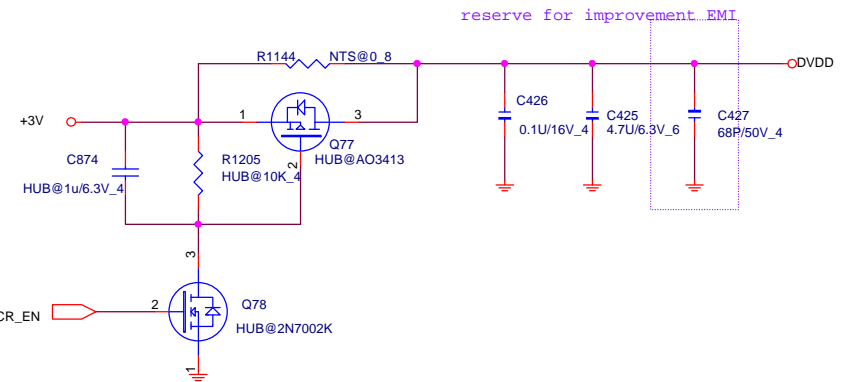
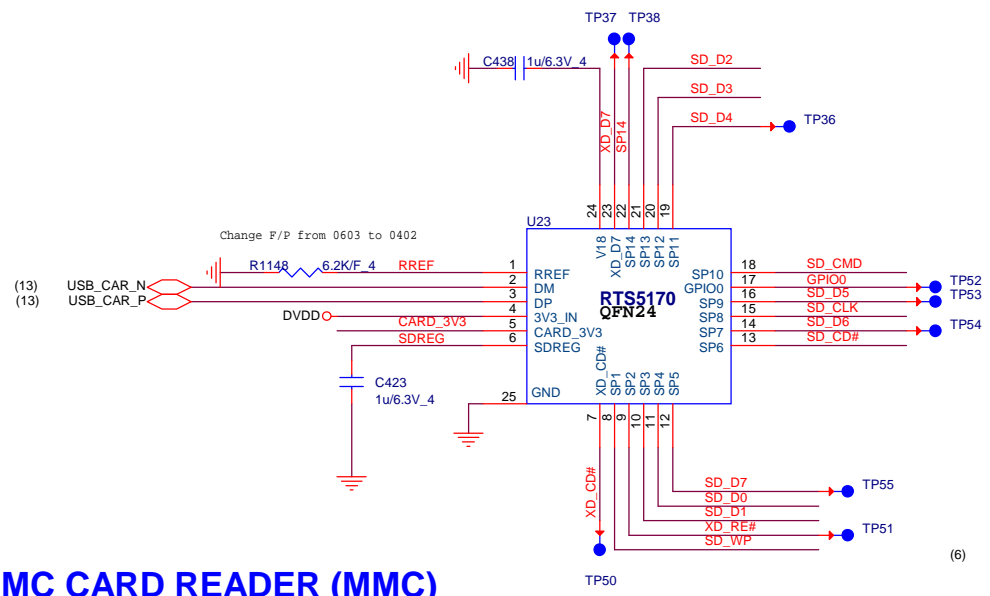


HP_MIC 上/下/左/右包覆AGND

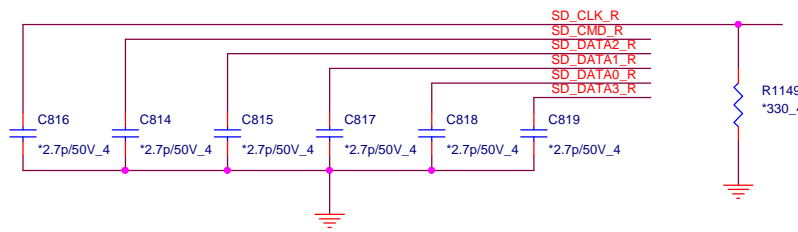
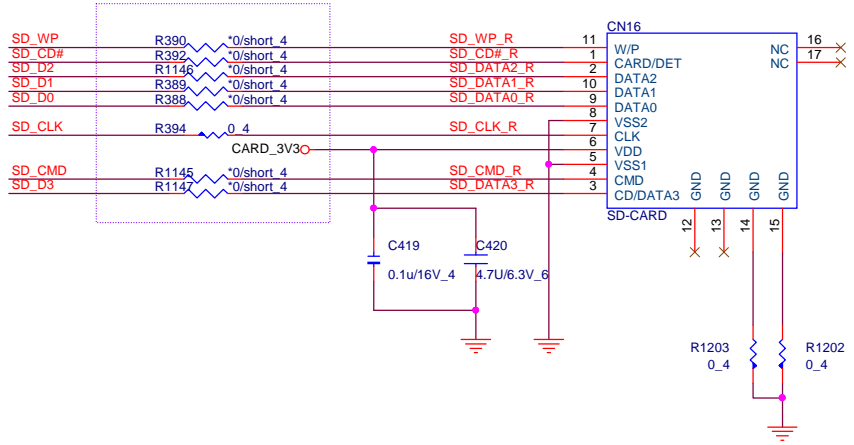



```
Normal Open
PIN1 --> L
PIN2 --> R
PIN3 --> GND/MIC
PIN4 --> MIC/GND
PIN5 --> GND
PIN6 --> JD
PIN7 --> Shielding
```





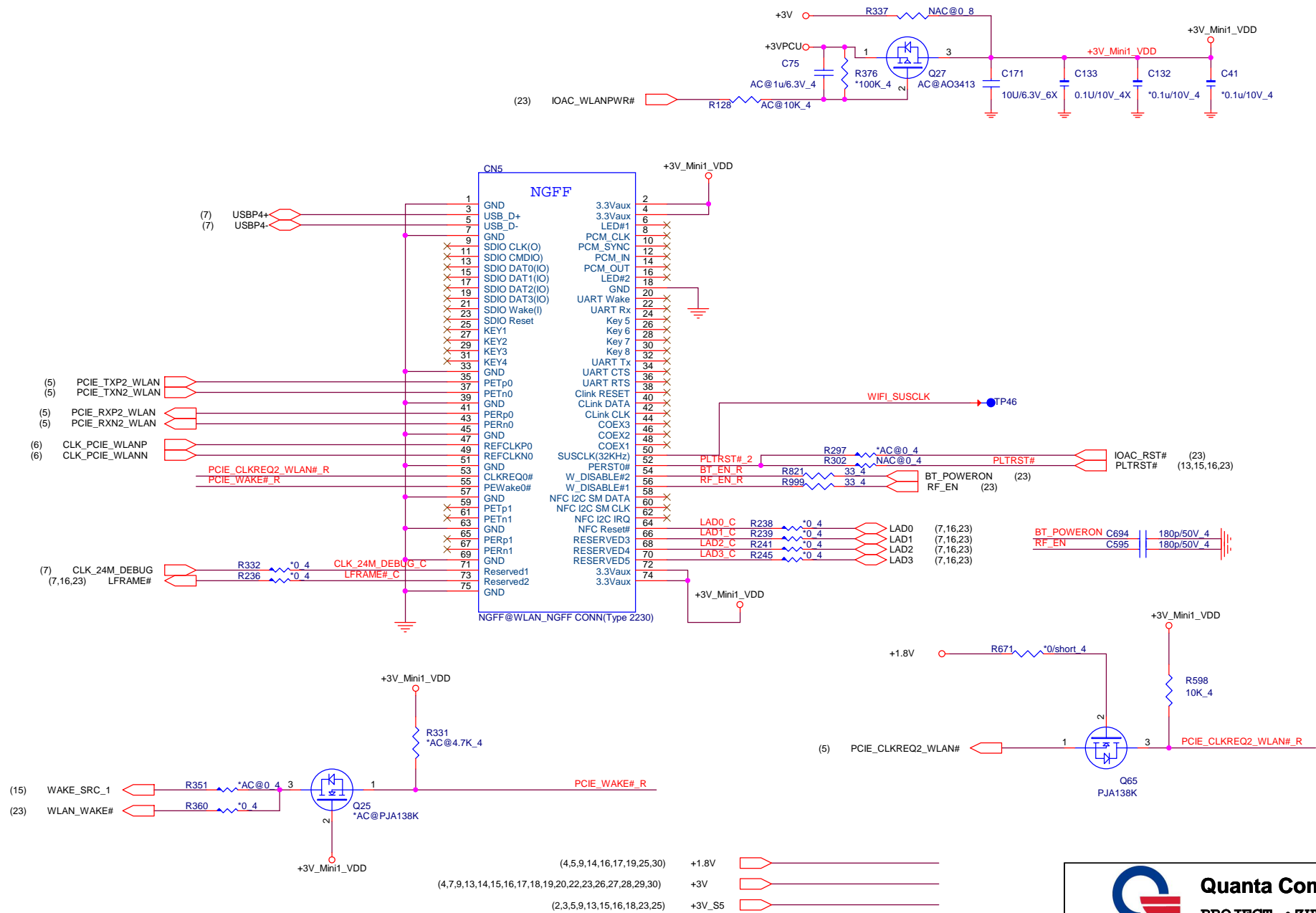
SD/MMC CARD READER (MMC)

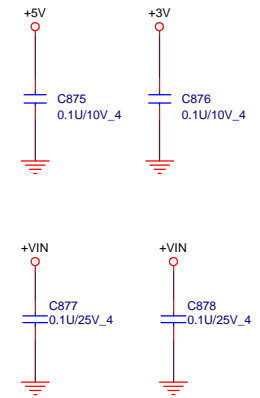
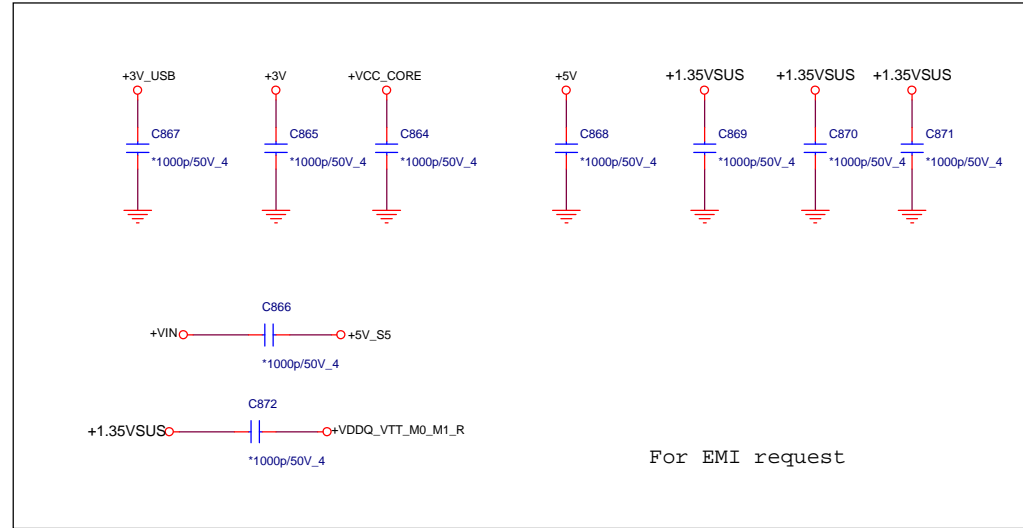
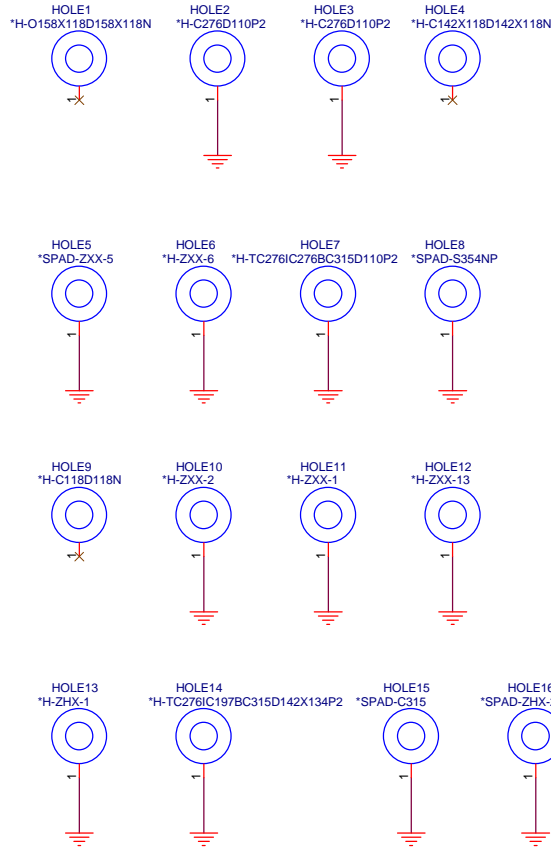




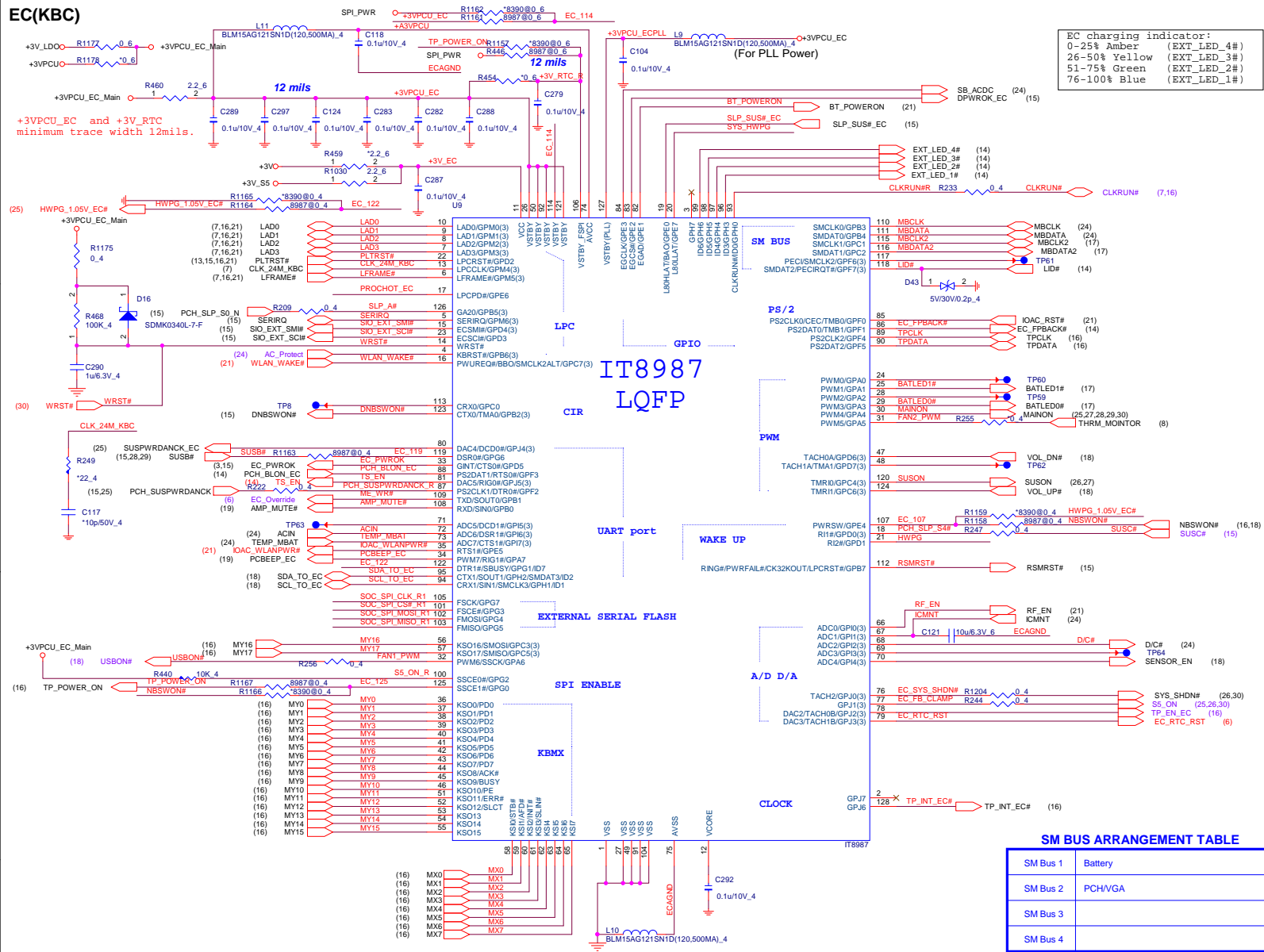
Quanta Computer Inc.
PROJECT : ZHW_ZHX

Size	Document Number	Rev
	Cardreader GL834L	1A
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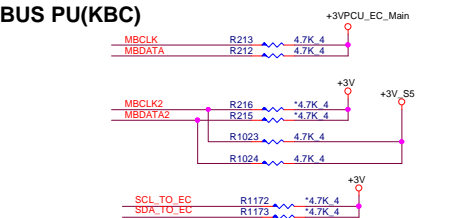




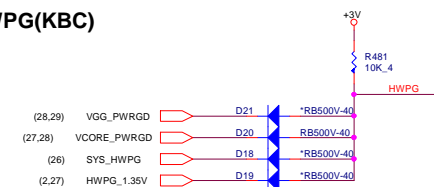
EC(KBC)



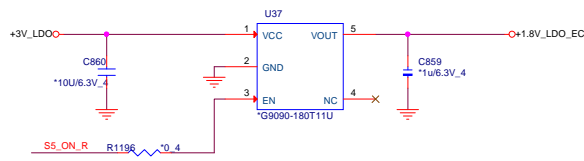
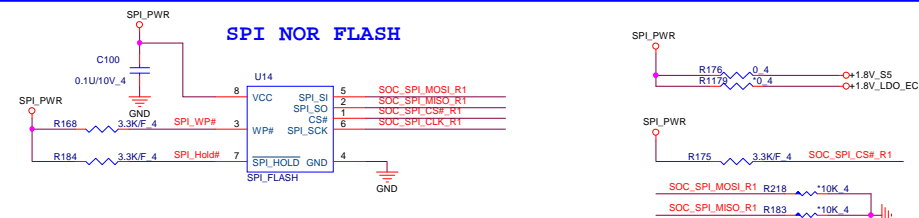
SM BUS PU(KBC)

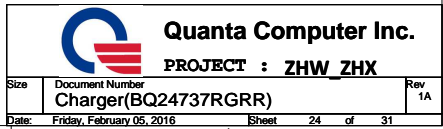


HWPG(KBC)

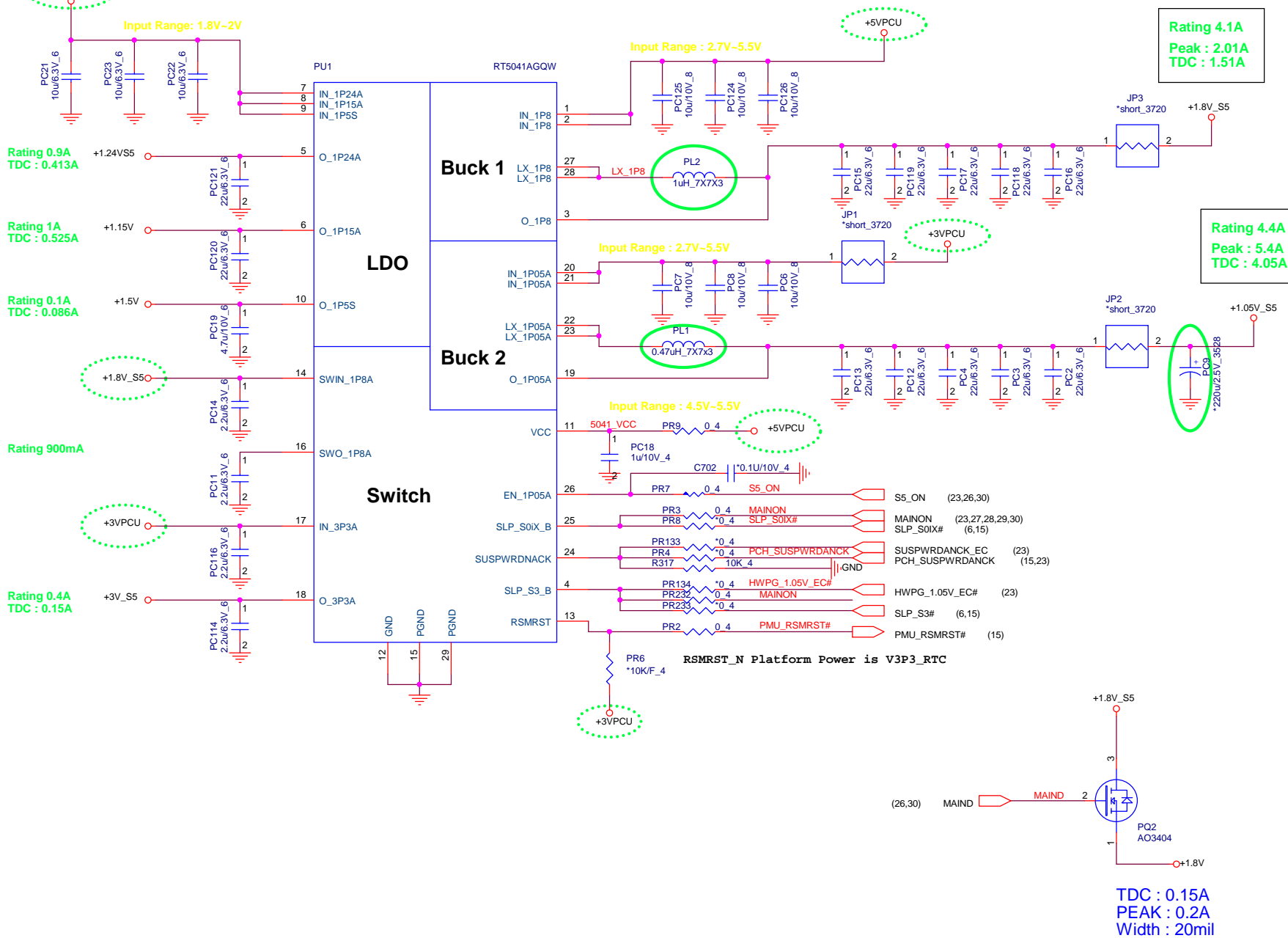


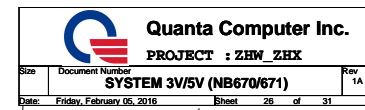
SPI NOR FLASH





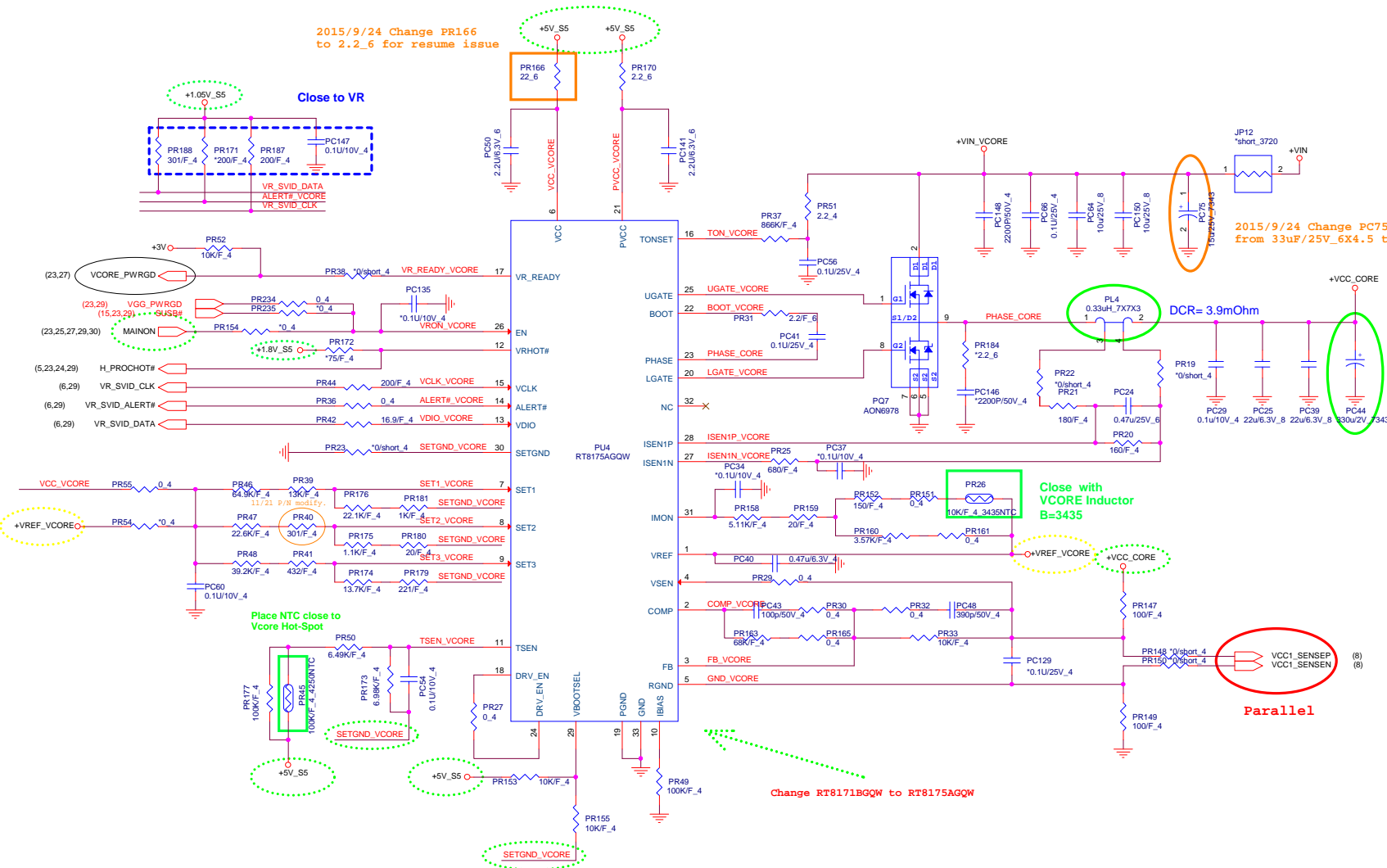
When SLP_S0iX_B=High, LDO_V1P15A_VOUT=1.15V
 When SLP_S0iX_B=LOW, LDO_V1P15A_VOUT=0.75V





2015/9/24 Change PR166
to 2.2_6 for resume issue

Close to VR



VR 12.1

Braswell - VCC0+1 (1 Phase)

Icc TDC PL2 : TBD

Icc Max : 7A

OCP : 12.4A

Fsw : 800KHz

Vboot : 1V

VR address : 0

VCC0+1 L/L :

R_DC_LL : 0 mV/A

R_AC_LL : 0 mV/A



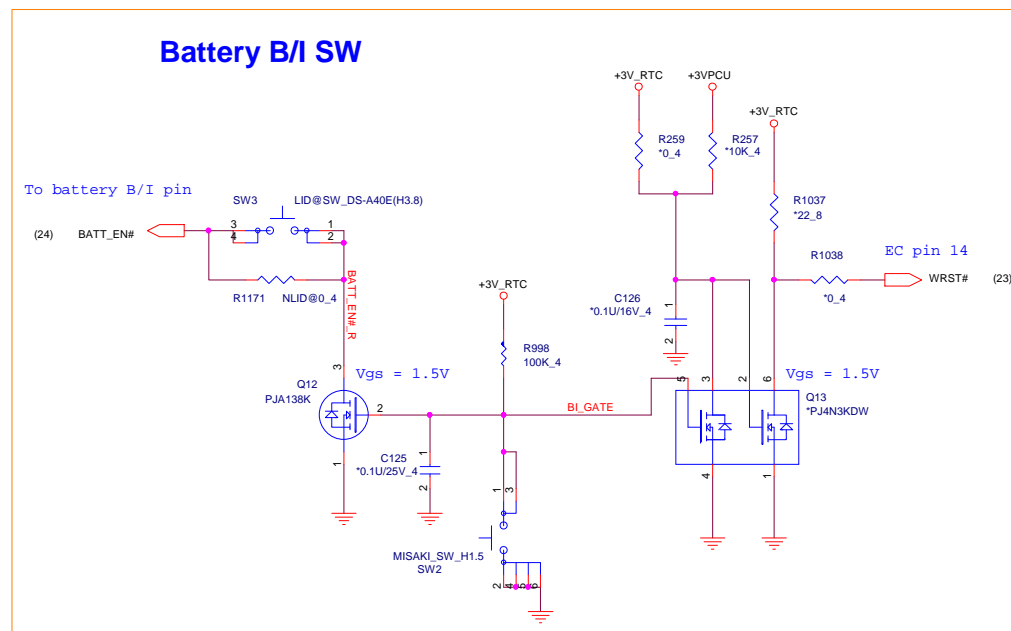
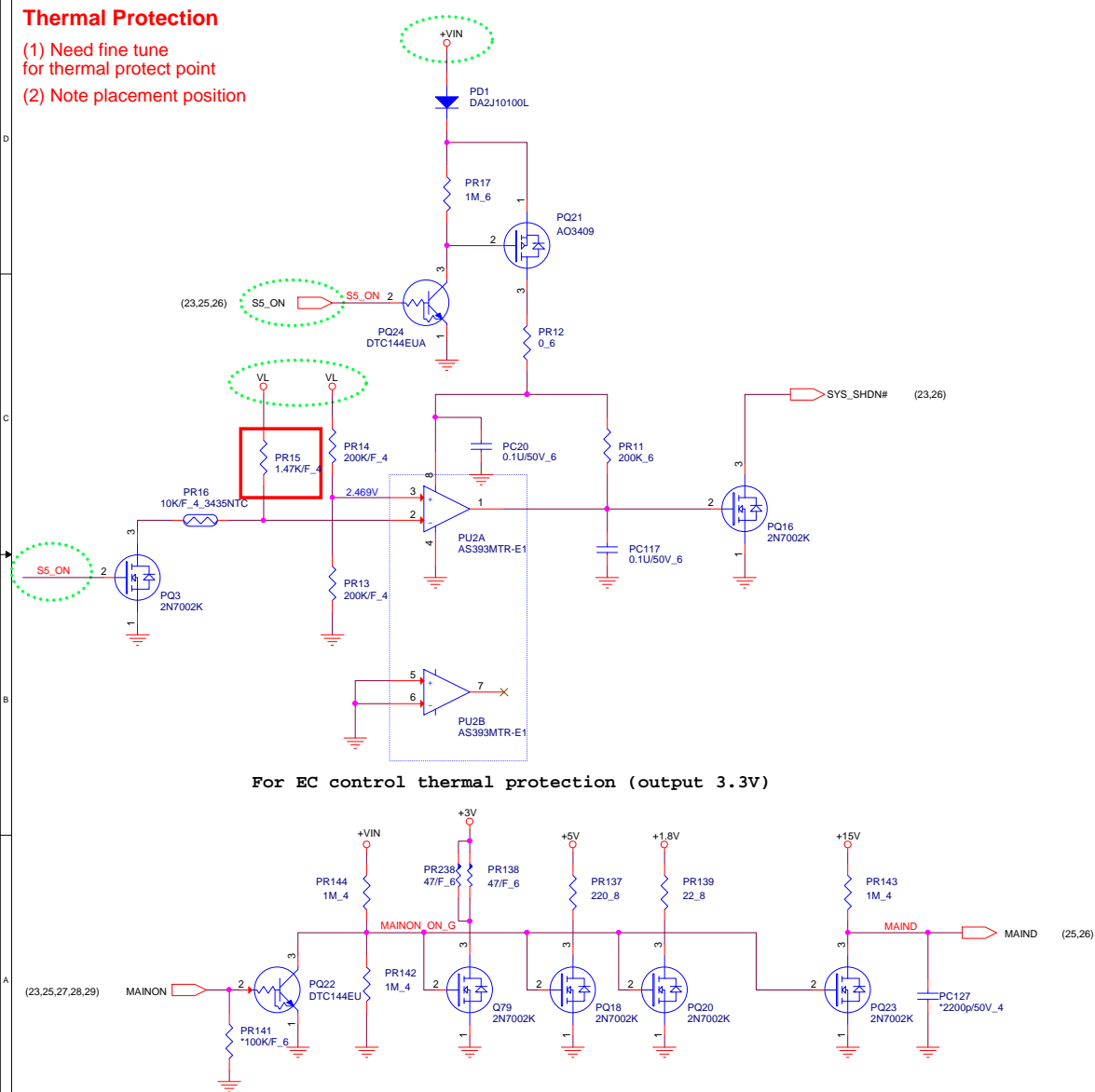
Quanta Computer Inc.

PROJECT : ZHW_ZHX


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	VCC0+1 (RT8175)	1A
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Thermal Protection

- (1) Need fine tune for thermal protect point
- (2) Note placement position



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Size	Document Number	Thermal / Discharge	
Date:	Friday, February 05, 2016	Sheet	30 of 31

Model	Rev.	CHANGE LIST				
ZHKD	1A	2014/12/11 First Release				
<div> <div>DOC NO.</div> <div>PROJECT MODEL :</div> <div>ZHKD</div> <div>APPROVED BY:</div> <div>DATE:</div> </div> <div> <div>PART NUMBER:</div> <div>DRAWING BY:</div> <div>REVISION:</div> </div> <div>  <div> <div>Quanta Computer Inc.</div> <div>PROJECT : ZHW_2008</div> <div>Change list-1</div> </div> </div>						